

## Rohit Galatage

- Objective** To perform excellently in research directly applicable to semiconductor industry.
- Research Interest** Future semiconductor materials, high-k dielectric materials on compound semiconductors
- Academics**  
**The University of Texas at Dallas**, Richardson, Texas  
Master of Science in Electrical Engineering- Microelectronics (Current)  
May 2009  
  
**Textile and Engineering Institute**, Ichalkaranji, Maharashtra, India.  
Bachelor of Engineering in Electronics, Overall GPA: 4.0  
June 2006
- Course work**  
**MS:** Fundamentals of Semiconductor Devices, Quantum physical electronics, Semiconductor processing technology.  
**BE:** Electronic devices and circuits, Digital systems, linear integrated circuits, Biomedical Instrumentation, System on Chip, Computer organization, VLSI design, Embedded systems.
- Software skills**  
**Operating Systems:** Windows 9x/Me/2000/XP, Red Hat Linux (novice)  
**Languages:** C, VHDL, Verilog, assembly language  
**Tools:** Orcad 9, Xilinx ISE, Modelsim
- Work experience**  
**University of Texas at Dallas, Richardson**  
**Graduate Research Assistant, Jan 2008-present**  
Will be working on high K dielectric materials for semiconductor devices  
  
**Persistent Systems Pvt. Ltd, India**  
VLSI Verification Engineer Jan 2007 – June 2007  
Verification of the digital chips using verilog and system verilog
- Projects**  
**RISC microcontroller design in FPGA**  
Designed a partial 8 bit RISC microcontroller. Op Code length is of 16 bits with format source1, source2, instruction code, and destination. It supports 16 different arithmetic, logical and data transfer instructions but no branch instruction.
- Special achievements**  
-Recipient of Shivaji University Merit Scholarship for the year 2003-2004, 2004-2005 and 2005-06.  
-Honored as the best student in Electronics Dept. for the year 2005-2006.  
-Ranked **fourth** in the university in the final year.